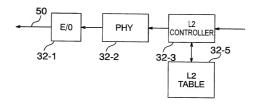
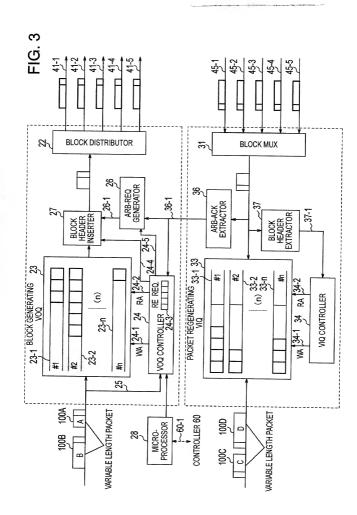


FIG. 2B





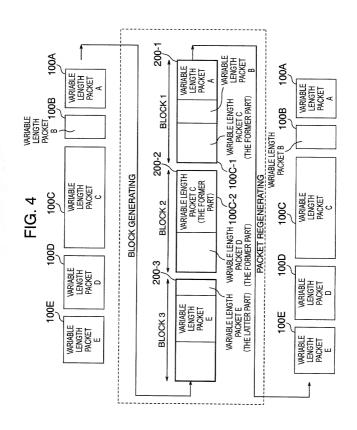


FIG. 5

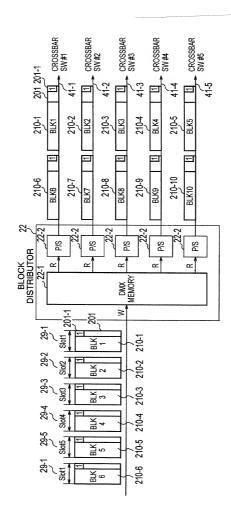
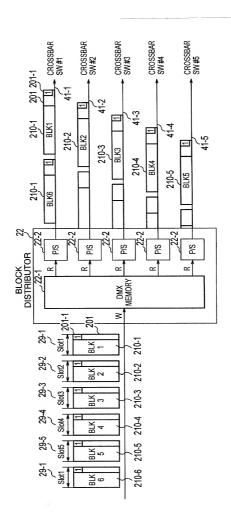


FIG. 6



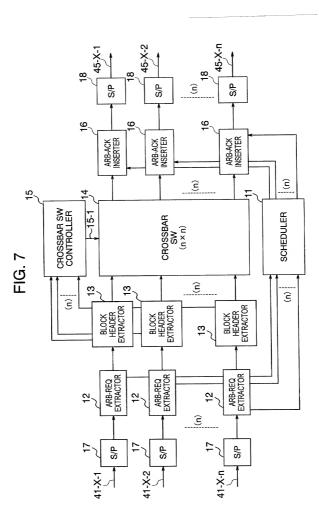
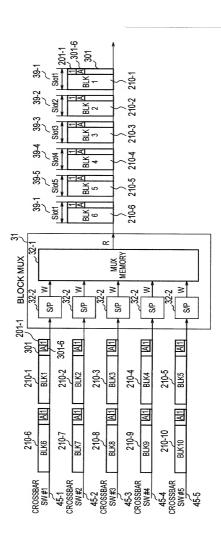


FIG. 8



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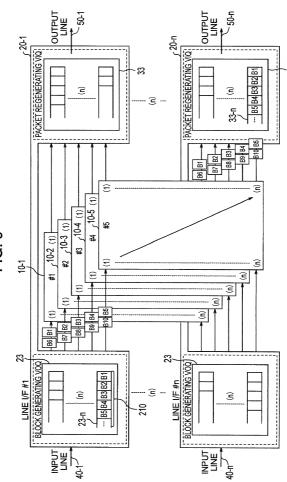


FIG. 9

FIG. 10

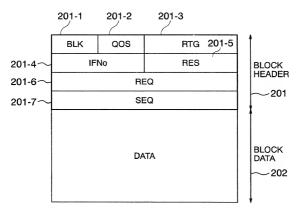


FIG. 11

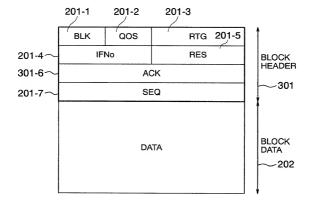


FIG. 12

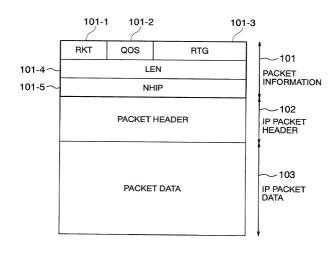


FIG. 13

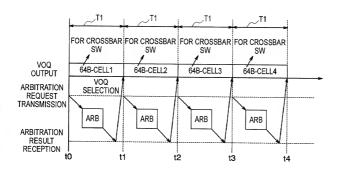


FIG. 14

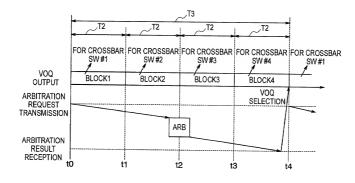
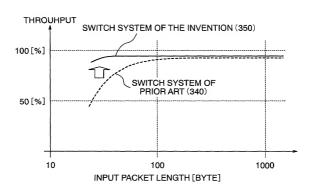


FIG. 15





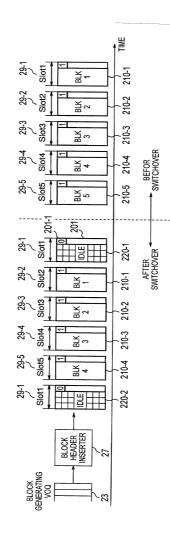
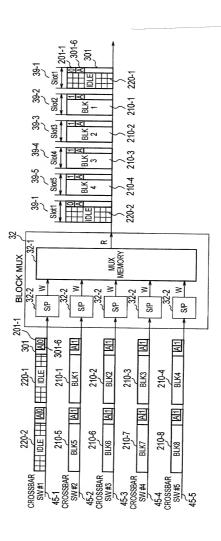


FIG. 17



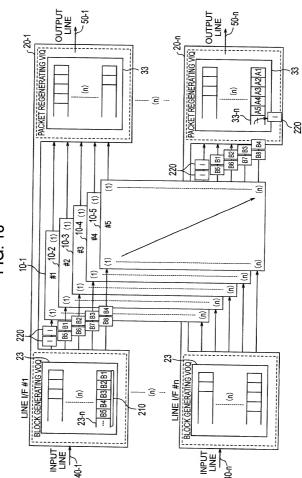


FIG. 18

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FIG. 19

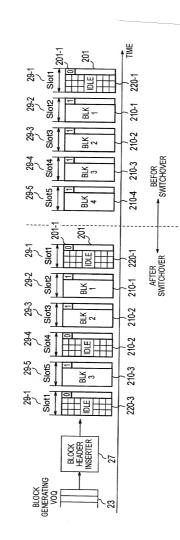
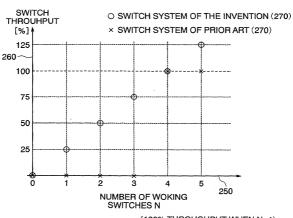


FIG. 20



(100% THROUGHPUT WHEN N=4)

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FIG. 21

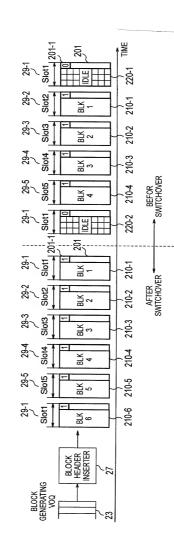
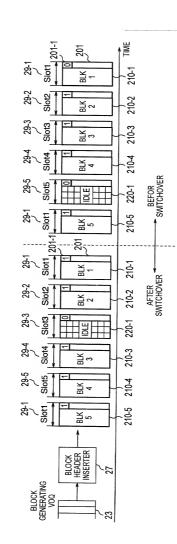


FIG. 22



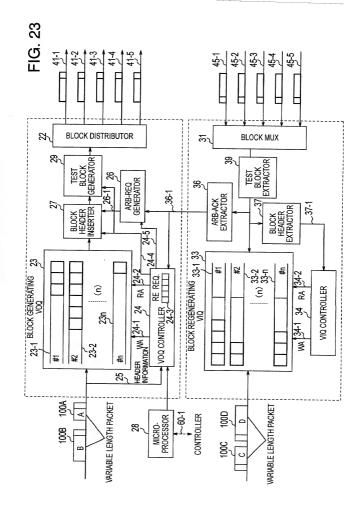


FIG. 24

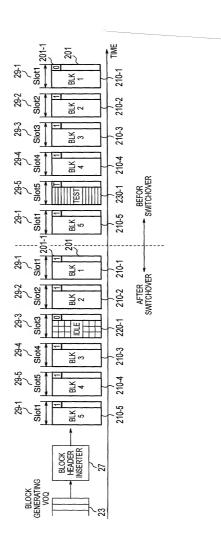


FIG. 25

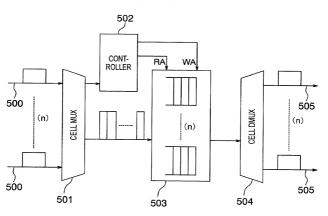


FIG. 26

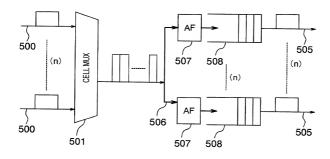
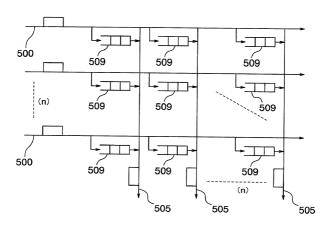


FIG. 27



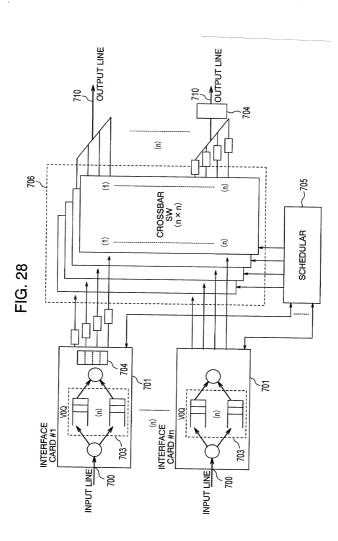


FIG. 29

